32-STAGE STATIC LEFT/RIGHT SHIFT REGISTER

- FULLY STATIC OPERATION
- SHIFT LEFT/SHIFT RIGHT CAPABILITY
- MULTIPLE PACKAGE CASCADING
- RECIRCULATE CAPABILITY
- LIFO OR FIFO CAPABILITY
- STANDARDIZED SYMMETRICAL OUTPUT CHARACTERISTICS
- QUIESCENT CURRENT SPECIFIED AT 20V FOR HCC DEVICE
- 5V, 10V, AND 15V PARAMETRIC RATINGS
- INPUT CURRENT OF 100nA AT 18V AND 25°C FOR HCC DEVICE
- 100% TESTED FOR QUIESCENT CURRENT
- MEETS ALL REQUIREMENTS OF JEDEC TENTATIVE STANDARD NO. 13A, "STANDARD SPECIFICATIONS FOR DESCRIPTION OF "B" SERIES CMOS DEVICES"

DESCRIPTION
The HCC40100B (extended temperature range) and HCF40100B (intermediate temperature range) are monolithic integrated circuits, available in 16-lead dual in-line plastic or ceramic package and plastic micro package. The HCC/HCF40100B is a 32-stage shift register containing 32 D-type master-slave flip-flops. The data present at the SHIFT-RIGHT INPUT is transferred into the first register stage synchronously with the positive CLOCK edge, provided the LEFT/RIGHT CONTROL is at a low level, the RECIRCULATE CONTROL is at a high level, and the CLOCK INHIBIT is low. If the LEFT/RIGHT CONTROL is at a high level and the RECIRCULATE CONTROL is also high, data at the SHIFT-LEFT INPUT is transferred into the 32nd register stage synchronously with the positive CLOCK transition, provided the CLOCK INHIBIT is low. The state of the LEFT/RIGHT CONTROL, RECIRCULATE CONTROL, and CLOCK INHIBIT should not be changed when the CLOCK is high. Data is shifted one stage left or one stage right depending on the state of the LEFT/RIGHT CONTROL, synchronously with the positive CLOCK edge. Data clocked into the first or 32nd register states is available at the SHIFT-LEFT or SHIFT-RIGHT OUTPUT respectively, on the next negative CLOCK transition (see Data Transfer Table). No shifting occurs on the positive CLOCK edge if the CLOCK INHIBIT line is at a high level. With the RECIRCULATE CONTROL low, data in the 32nd stage is shifted into the first stage when the LEFT/RIGHT CONTROL is low and from the 1st stage to the 32nd stage when the LEFT/RIGHT CONTROL is high.

ORDER CODES:
HCC40100BF  HCF40100BM1
HCF40100BEY  HCF40100BC1

PIN CONNECTIONS

- CLK
- CLOCK INHIBIT
- SHIFT LEFT OUT
- SHIFT LEFT IN
- RECYCULATE
- Vdd
- VSS
- 1
- 2
- 3
- 4
- 5
- 6
- 7
- 8
- 9
- 10
- 11
- 12
- 13
- 14
- 15
- 16

NC = NO CONNECTION
FUNCTIONAL DIAGRAM

ABSOLUTE MAXIMUM RATINGs

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Value</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>VDD*</td>
<td>Supply Voltage : HCC Types</td>
<td>− 0.5 to + 20</td>
<td>V</td>
</tr>
<tr>
<td></td>
<td>HCF Types</td>
<td>− 0.5 to + 18</td>
<td>V</td>
</tr>
<tr>
<td>VI</td>
<td>Input Voltage</td>
<td>− 0.5 to VDD + 0.5</td>
<td>V</td>
</tr>
<tr>
<td>Ii</td>
<td>DC Input Current (any one input)</td>
<td>± 10</td>
<td>mA</td>
</tr>
<tr>
<td>Ptot</td>
<td>Total Power Dissipation (per package)</td>
<td>200</td>
<td>mW</td>
</tr>
<tr>
<td></td>
<td>Dissipation per Output Transistor for T_op = Full Package-temperature Range</td>
<td>100</td>
<td>mW</td>
</tr>
<tr>
<td>Top</td>
<td>Operating Temperature : HCC Types</td>
<td>− 55 to + 125</td>
<td>°C</td>
</tr>
<tr>
<td></td>
<td>HCF Types</td>
<td>− 40 to + 85</td>
<td>°C</td>
</tr>
<tr>
<td>Tstg</td>
<td>Storage Temperature</td>
<td>− 65 to + 150</td>
<td>°C</td>
</tr>
</tbody>
</table>

Stresses above those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for external periods may affect device reliability.

RECOMMENDED OPERATING CONDITIONS

<table>
<thead>
<tr>
<th>Symbol</th>
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<th>Value</th>
<th>Unit</th>
</tr>
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<tbody>
<tr>
<td>VDD</td>
<td>Supply Voltage : HCC Types</td>
<td>3 to + 18</td>
<td>V</td>
</tr>
<tr>
<td></td>
<td>HCF Types</td>
<td>3 to + 15</td>
<td>V</td>
</tr>
<tr>
<td>VI</td>
<td>Input Voltage</td>
<td>0 to VDD</td>
<td>V</td>
</tr>
<tr>
<td>Top</td>
<td>Operating Temperature : HCC Types</td>
<td>− 55 to + 125</td>
<td>°C</td>
</tr>
<tr>
<td></td>
<td>HCF Types</td>
<td>− 40 to + 85</td>
<td>°C</td>
</tr>
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</table>
HCC/HCF40100B

TRUTH TABLES

CONTROL

<table>
<thead>
<tr>
<th>Left/Right Control</th>
<th>Clock Inhibit</th>
<th>Recirculate Control</th>
<th>Action</th>
<th>Input Bit Origin</th>
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<tbody>
<tr>
<td>1</td>
<td>0</td>
<td>1</td>
<td>Shift Left</td>
<td>Shift Left Input</td>
</tr>
<tr>
<td>1</td>
<td>0</td>
<td>0</td>
<td>Shift Left</td>
<td>Stage 1</td>
</tr>
<tr>
<td>0</td>
<td>0</td>
<td>1</td>
<td>Shift Right</td>
<td>Shift Right Input</td>
</tr>
<tr>
<td>0</td>
<td>0</td>
<td>0</td>
<td>Shift Right</td>
<td>Stage 32</td>
</tr>
<tr>
<td>X</td>
<td>1</td>
<td>X</td>
<td>No Shift</td>
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DATA TRANSFER

<table>
<thead>
<tr>
<th>Initial State</th>
<th>Clock</th>
<th>Resulting State</th>
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<tbody>
<tr>
<td>Data Input</td>
<td>Clock Inhibit</td>
<td>Internal Stage</td>
</tr>
<tr>
<td>0</td>
<td>0</td>
<td>X</td>
</tr>
<tr>
<td>X</td>
<td>0</td>
<td>X</td>
</tr>
<tr>
<td>1</td>
<td>0</td>
<td>X</td>
</tr>
<tr>
<td>X</td>
<td>0</td>
<td>1</td>
</tr>
<tr>
<td>X</td>
<td>1</td>
<td>X</td>
</tr>
</tbody>
</table>

0 = Low level 1 = High level X = Don't Care. NC = No change.

* For Shift-Right Mode
  Data Input = SHIFT-RIGHT INPUT (Pin 11)
  Internal Stage = Stage 1 (Q1)
  Output = SHIFT-LEFT OUTPUT (Pin 4).

For Shift-left Mode
  Data Input = SHIFT LEFT INPUT (Pin 6)
  Internal Stage = Stage 32 (Q32)
  Output = SHIFT-RIGHT OUTPUT (Pin 12).

STATIC ELECTRICAL CHARACTERISTICS (over recommended operating conditions)

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Test Conditions</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td>V_I (V)</td>
<td>V_O (V)</td>
</tr>
<tr>
<td>I_L</td>
<td>Quiescent Current</td>
<td>HCC Types</td>
<td>0/5</td>
</tr>
<tr>
<td></td>
<td></td>
<td>HCF Types</td>
<td>0/10</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>0/15</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>0/20</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>0/5</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>0/10</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>0/15</td>
</tr>
<tr>
<td>V_OH</td>
<td>Output High Voltage</td>
<td>0/5</td>
<td>&lt;1</td>
</tr>
<tr>
<td></td>
<td></td>
<td>0/10</td>
<td>&lt;1</td>
</tr>
<tr>
<td></td>
<td></td>
<td>0/15</td>
<td>&lt;1</td>
</tr>
<tr>
<td>V_OL</td>
<td>Output Low Voltage</td>
<td>5/0</td>
<td>&lt;1</td>
</tr>
<tr>
<td></td>
<td></td>
<td>10/0</td>
<td>&lt;1</td>
</tr>
<tr>
<td></td>
<td></td>
<td>15/0</td>
<td>&lt;1</td>
</tr>
</tbody>
</table>

* T_Low = –55⁰C for HCC device: –40⁰C for HCF device.
* T_High = +125⁰C for HCC device: +85⁰C for HCF device.

The Noise Margin for both “1” and “0” level is: 1V min. with V_DD = 5V, 2V min. with V_DD = 10V, 2.5 V min. with V_DD = 15V.

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### STATIC ELECTRICAL CHARACTERISTICS (continued)

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Test Conditions</th>
<th>Value</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td>$V_I$ (V) $V_O$ (V) $I_O$ (µA) $V_DD$ (V)</td>
<td>$T_{Low}^{*}$</td>
<td>25°C</td>
</tr>
<tr>
<td>$V_{IH}$</td>
<td>Input High Voltage</td>
<td>0.5/4.5</td>
<td>&lt; 1</td>
<td>5</td>
</tr>
<tr>
<td></td>
<td></td>
<td>1/9</td>
<td>&lt; 1</td>
<td>10</td>
</tr>
<tr>
<td></td>
<td></td>
<td>1.5/13.5</td>
<td>&lt; 1</td>
<td>15</td>
</tr>
<tr>
<td>$V_{IL}$</td>
<td>Input Low Voltage</td>
<td>4.5/0.5</td>
<td>&lt; 1</td>
<td>5</td>
</tr>
<tr>
<td></td>
<td></td>
<td>9/1</td>
<td>&lt; 1</td>
<td>10</td>
</tr>
<tr>
<td></td>
<td></td>
<td>13.5/1.5</td>
<td>&lt; 1</td>
<td>15</td>
</tr>
<tr>
<td>$I_{OH}$</td>
<td>Output Drive Current</td>
<td>HCC Types</td>
<td>0/5</td>
<td>2.5</td>
</tr>
<tr>
<td></td>
<td></td>
<td>HCF Types</td>
<td>0/5</td>
<td>4.6</td>
</tr>
<tr>
<td></td>
<td></td>
<td>0/10</td>
<td>9.5</td>
<td>10</td>
</tr>
<tr>
<td></td>
<td></td>
<td>0/15</td>
<td>13.5</td>
<td>15</td>
</tr>
<tr>
<td>$I_{OL}$</td>
<td>Output Sink Current</td>
<td>HCC Types</td>
<td>0/5</td>
<td>0.4</td>
</tr>
<tr>
<td></td>
<td></td>
<td>HCF Types</td>
<td>0/5</td>
<td>0.5</td>
</tr>
<tr>
<td></td>
<td></td>
<td>0/10</td>
<td>0.5</td>
<td>10</td>
</tr>
<tr>
<td></td>
<td></td>
<td>0/15</td>
<td>1.5</td>
<td>15</td>
</tr>
<tr>
<td>$I_{IH}$, $I_{IL}$</td>
<td>Input Leakage Current</td>
<td>HCC Types</td>
<td>0/18</td>
<td>18</td>
</tr>
<tr>
<td></td>
<td></td>
<td>HCF Types</td>
<td>0/15</td>
<td>15</td>
</tr>
<tr>
<td>$C_I$</td>
<td>Input Capacitance</td>
<td>Any Input</td>
<td>5</td>
<td>7.5</td>
</tr>
</tbody>
</table>

* $T_{Low} = 55°C$ for HCC device : $= 40°C$ for HCF device.
* $T_{High} = 125°C$ for HCC device : $= 85°C$ for HCF device.
* The Noise Margin for both "1" and "0" level is : 1V min. with $V_{DD} = 5V$, 2V min. with $V_{DD} = 10V$, 2.5 V min. with $V_{DD} = 15V$.

### DYNAMIC ELECTRICAL CHARACTERISTICS ($T_{amb} = 25°C$, $C_L = 50pF$, $R_L = 200kΩ$, typical temperature coefficient for all $V_{DD}$ values is 0.3%/°C, all input rise and fall time = 20ns)

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Test Conditions</th>
<th>Value</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td>$V_{DD}$ (V)</td>
<td>Min.</td>
<td>Typ.</td>
</tr>
<tr>
<td>$t_{PLH}$, $t_{PHL}$</td>
<td>Propagation Delay Time</td>
<td>5</td>
<td>360</td>
<td>720</td>
</tr>
<tr>
<td></td>
<td>Clock to Shift Left/Right Output</td>
<td>10</td>
<td>165</td>
<td>330</td>
</tr>
<tr>
<td></td>
<td></td>
<td>15</td>
<td>115</td>
<td>230</td>
</tr>
<tr>
<td>$t_{THL}$, $t_{TLH}$</td>
<td>Transition Time</td>
<td>5</td>
<td>100</td>
<td>200</td>
</tr>
<tr>
<td></td>
<td></td>
<td>10</td>
<td>50</td>
<td>100</td>
</tr>
<tr>
<td></td>
<td></td>
<td>15</td>
<td>40</td>
<td>80</td>
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SGS-THOMSON MICROELECTRONICS

5/13
### Dynamic Electrical Characteristics (continued)

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Test Conditions</th>
<th>Value</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td>$V_{DD}$ (V)</td>
<td>Min.</td>
<td>Typ.</td>
</tr>
<tr>
<td>$t_{	ext{setup}}$</td>
<td>Data Setup Time</td>
<td>5</td>
<td>100</td>
<td>50</td>
</tr>
<tr>
<td></td>
<td></td>
<td>10</td>
<td>20</td>
<td>10</td>
</tr>
<tr>
<td></td>
<td></td>
<td>15</td>
<td>10</td>
<td>5</td>
</tr>
<tr>
<td>$t_{	ext{hold}}$</td>
<td>Data Hold Time</td>
<td>5</td>
<td>275</td>
<td>170</td>
</tr>
<tr>
<td></td>
<td></td>
<td>10</td>
<td>100</td>
<td>75</td>
</tr>
<tr>
<td></td>
<td></td>
<td>15</td>
<td>75</td>
<td>50</td>
</tr>
<tr>
<td>$t_W$</td>
<td>Clock Input Pulse Width Low Level</td>
<td>5</td>
<td>450</td>
<td>225</td>
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<td>115</td>
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<td></td>
<td>15</td>
<td>190</td>
<td>95</td>
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<tr>
<td>$t_W$</td>
<td>Clock Input Pulse Width High Level</td>
<td>5</td>
<td>280</td>
<td>140</td>
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<td>75</td>
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<td></td>
<td></td>
<td>15</td>
<td>140</td>
<td>70</td>
</tr>
<tr>
<td>$f_{CL}$</td>
<td>Maximum Clock Input Frequency</td>
<td>5</td>
<td>1</td>
<td>2</td>
</tr>
<tr>
<td></td>
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<td>10</td>
<td>2.5</td>
<td>5</td>
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<tr>
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<td></td>
<td>15</td>
<td>3</td>
<td>6</td>
</tr>
</tbody>
</table>

### Waveforms

![Waveforms Diagram](image)
Output Low (sink) Current Characteristics.

Output High (source) Current Characteristics.

Typical Propagation Delay Time (clock to shift left right) vs. Load Capacitance.

Typical Transition Time vs. Load Capacitance.

Typical Dynamic Power Dissipation vs. Clock Frequency.
HCC/HCF40100B

TEST CIRCUITS

Quiescent Device Current.

Input Leakage Current.

Input Voltage.

NOTE: MEASURE INPUTS SEQUENTIALLY TO BOTH VDD AND VSS. CONNECT ALL UNUSED INPUTS TO EITHER VDD OR VSS.
### Plastic DIP16 (0.25) MECHANICAL DATA

<table>
<thead>
<tr>
<th>DIM.</th>
<th>mm</th>
<th>inch</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>MIN.</td>
<td>TYP.</td>
</tr>
<tr>
<td>a1</td>
<td>0.51</td>
<td></td>
</tr>
<tr>
<td>B</td>
<td>0.77</td>
<td>1.65</td>
</tr>
<tr>
<td>b</td>
<td>0.5</td>
<td></td>
</tr>
<tr>
<td>b1</td>
<td>0.25</td>
<td></td>
</tr>
<tr>
<td>D</td>
<td></td>
<td>20</td>
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<tr>
<td>E</td>
<td>8.5</td>
<td></td>
</tr>
<tr>
<td>e</td>
<td>2.54</td>
<td></td>
</tr>
<tr>
<td>e3</td>
<td>17.78</td>
<td></td>
</tr>
<tr>
<td>F</td>
<td>7.1</td>
<td></td>
</tr>
<tr>
<td>I</td>
<td>5.1</td>
<td></td>
</tr>
<tr>
<td>L</td>
<td>3.3</td>
<td>1.27</td>
</tr>
</tbody>
</table>

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[Image of DIP16 package]
## Ceramic DIP16/1 MECHANICAL DATA

<table>
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</thead>
<tbody>
<tr>
<td></td>
<td>MIN.</td>
<td>TYP.</td>
</tr>
<tr>
<td></td>
<td>MIN.</td>
<td>TYP.</td>
</tr>
<tr>
<td>A</td>
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<tr>
<td>B</td>
<td>7</td>
<td>0.276</td>
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<tr>
<td>D</td>
<td>3.3</td>
<td>0.130</td>
</tr>
<tr>
<td>E</td>
<td>0.38</td>
<td>0.015</td>
</tr>
<tr>
<td>e3</td>
<td>17.78</td>
<td>0.700</td>
</tr>
<tr>
<td>F</td>
<td>2.29</td>
<td>0.090</td>
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<td>G</td>
<td>0.4</td>
<td>0.016</td>
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<tr>
<td>L</td>
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<td>0.009</td>
</tr>
<tr>
<td>M</td>
<td>0.51</td>
<td>0.020</td>
</tr>
<tr>
<td>N</td>
<td>10.3</td>
<td>0.406</td>
</tr>
<tr>
<td>P</td>
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<td>8.05</td>
</tr>
<tr>
<td>Q</td>
<td>5.08</td>
<td>0.200</td>
</tr>
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</table>
## SO16 (Narrow) MECHANICAL DATA

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</thead>
<tbody>
<tr>
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<td>MIN.</td>
<td>TYP.</td>
</tr>
<tr>
<td>A</td>
<td>1.75</td>
<td>0.068</td>
</tr>
<tr>
<td>a1</td>
<td>0.1</td>
<td>0.2</td>
</tr>
<tr>
<td>a2</td>
<td>1.65</td>
<td></td>
</tr>
<tr>
<td>b</td>
<td>0.35</td>
<td>0.46</td>
</tr>
<tr>
<td>b1</td>
<td>0.19</td>
<td>0.25</td>
</tr>
<tr>
<td>C</td>
<td>0.5</td>
<td></td>
</tr>
<tr>
<td>c1</td>
<td></td>
<td></td>
</tr>
<tr>
<td>D</td>
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![Diagram of SO16 (Narrow) mechanical data](image)
### PLCC20 MECHANICAL DATA

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