DESCRIPTION

The HEF4539B is a dual 4-input multiplexer with common select logic. Each multiplexer has four multiplexer inputs (I₀ and I₃), an active LOW enable input (E) and a multiplexer output (O). When HIGH, E forces O of the respective multiplexer LOW, independent of the select inputs (S₀ to S₁) and I₀ to I₃. When E is LOW, S₀ and S₁ determine which multiplexer input (I₀ to I₃) on each of the multiplexers is routed to the respective multiplexer output (O).

PINNING

I₀A, I₁A, I₂A, I₃A  multiplexer inputs
I₀B, I₁B, I₂B, I₃B  multiplexer inputs
S₀, S₁  select inputs
Eₐ, Eₐ  enable inputs (active LOW)
Oₐ, Oₐ  multiplexer outputs

FAMILY DATA, I_DD LIMITS category MSI

See Family Specifications
Dual 4-input multiplexer

HeF4539B

MSI

Fig. 3 Logic diagram.

FUNCTION TABLE

<table>
<thead>
<tr>
<th>INPUTS</th>
<th>OUTPUT</th>
</tr>
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<tbody>
<tr>
<td>S₀</td>
<td>S₁</td>
</tr>
<tr>
<td>X</td>
<td>X</td>
</tr>
<tr>
<td>L</td>
<td>L</td>
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<tr>
<td>H</td>
<td>L</td>
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<tr>
<td>L</td>
<td>H</td>
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<td>H</td>
<td>H</td>
</tr>
</tbody>
</table>

Notes
1. H = HIGH state (the more positive voltage)
2. L = LOW state (the less positive voltage)
3. X = state is immaterial